

MiiNePort E2 Schematic Design Guide

Second Edition, October 2010

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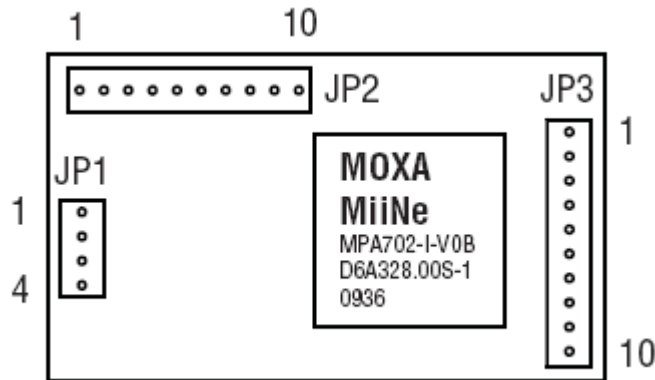
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Pin Description

MiiNePort E2 & E2-H (Bottom View)

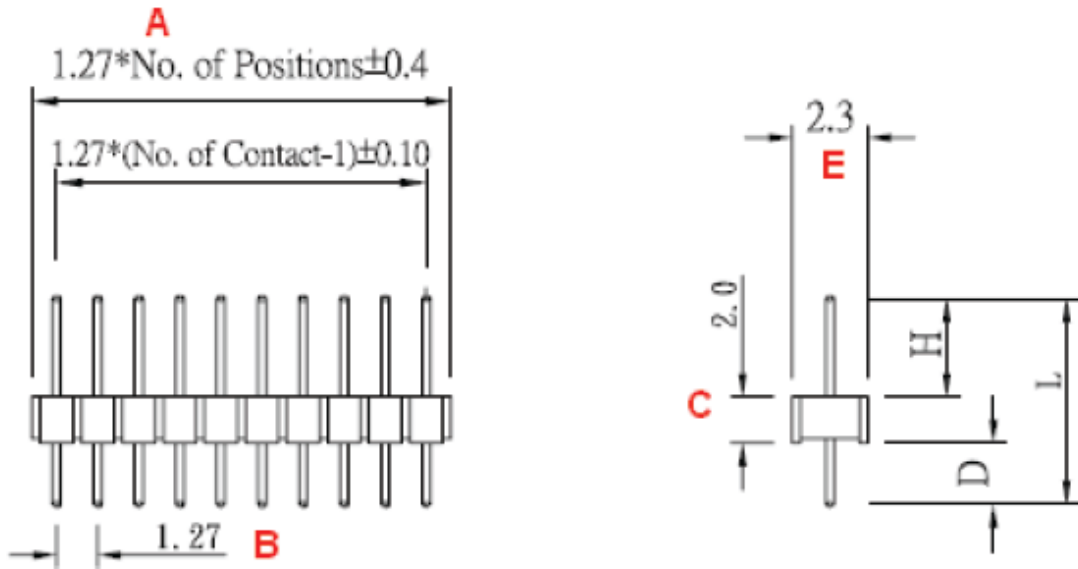


JP1			JP2		
Pin	Signal Name	Function	Pin	Signal Name	Function
1	Ethernet Tx+	Ethernet Transmit Data+	1	100M LED	Ethernet 100M LED
2	Ethernet Tx-	Ethernet Transmit Data-	2	10M LED	Ethernet 10M LED
3	Ethernet Rx+	Ethernet Receive Data+	3	LRXD	Receive Serial Data
4	Ethernet Rx-	Ethernet Receive Data-	4	LTXD	Transmit Serial Data
			5	LDCD	Receive Line Signal Detector
			6	RS485_EN	RS-485 Enable
			7	LRTS	Request To Send
			8	LDTR	Data Terminal Ready
			9	LDSR	Data Set Ready
			10	LCTS	Clear To Send

JP3		
Pin	Signal Name	Function
1	DIO0	Programmable I/O
2	DIO1	Programmable I/O
3	DIO2	Programmable I/O
4	DIO3	Programmable I/O
5	Reserve	N/A
6	Reserve	N/A
7	SW_Reset	Reset to Factory Default
8	GND	Circuit Ground
9	Ready LED	System To Ready LED
10	VCC	Power Supply

Recommended Mating Connector

JP1, JP2, JP3 pin header dimension

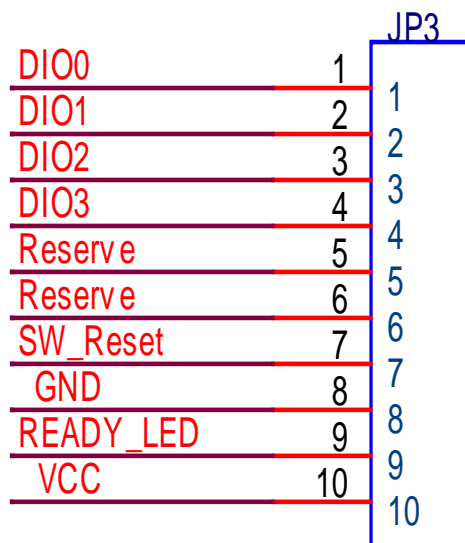


TOLERANCE: ± 0.25mm

Mechanism size Check/Compare (unit:mm)

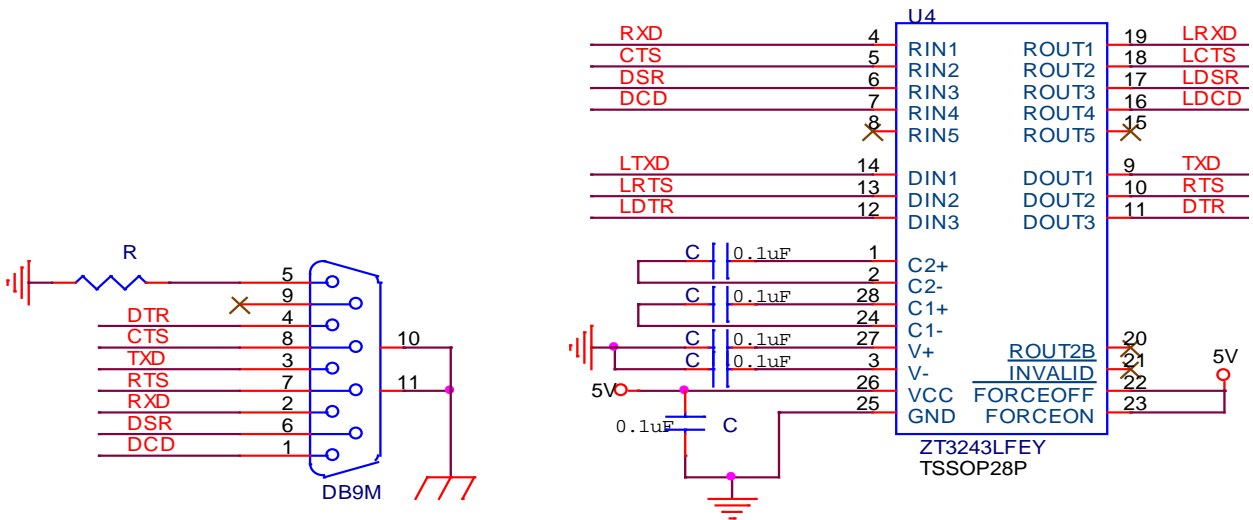
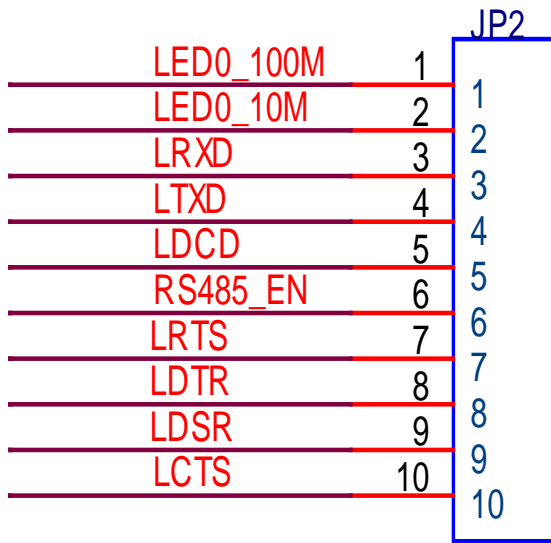
No.	Item	Spec	Tolerance	Location	Fact
1	A	3.81	±0.4	A	3.82
2	B	1.27	±0.25	B	1.25
3	C	2.0	±0.25	C	1.97
4	D	2.3	±0.25	D	2.31
5	E	2.3	±0.25	E	2.27
6	H	3.0	±0.25	H	2.97
7	L	7.3	±0.25	L	7.29

System Power Circuit Design

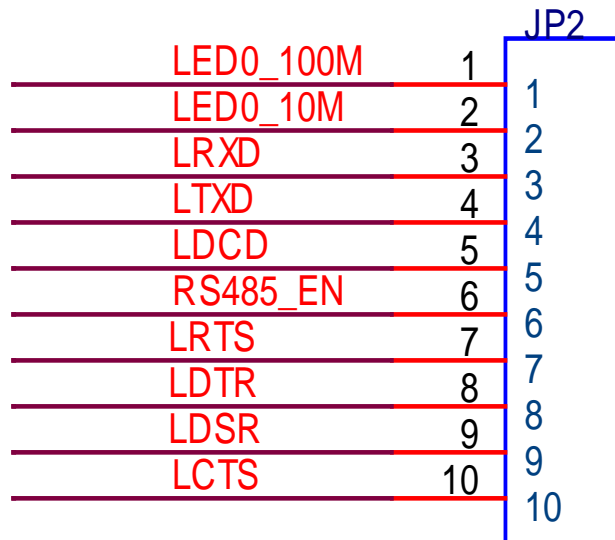


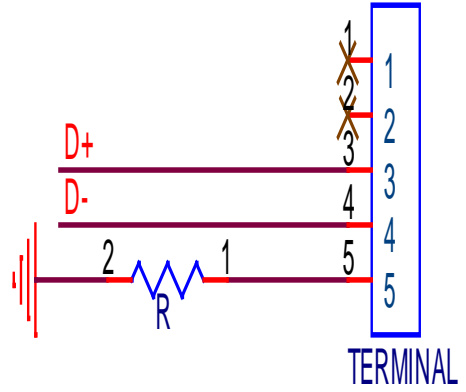
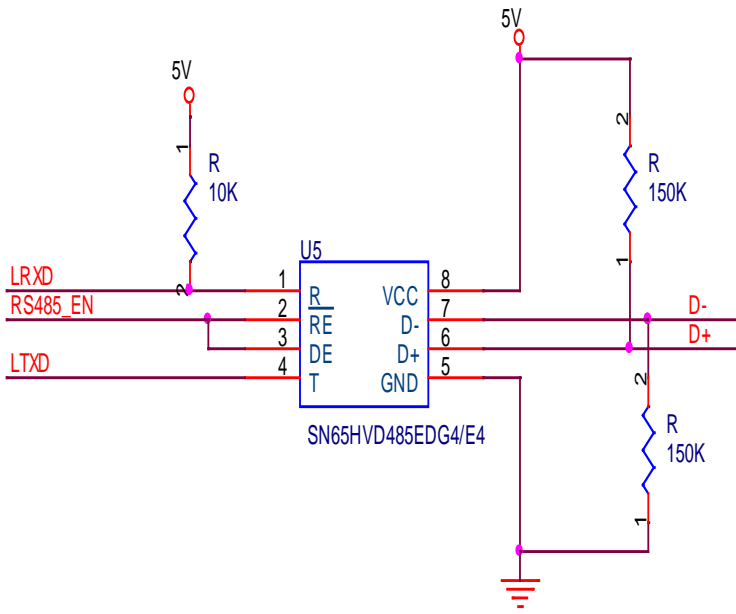
Symbol	Parameter	Min	Nominal	Max	Units
VCC	Supply Voltage	3.14	3.3~5	5.25	V

RS-232 Circuit Design

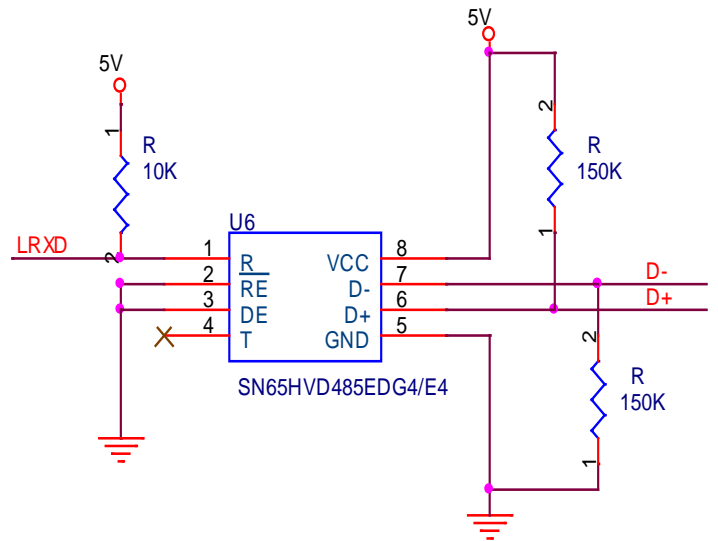
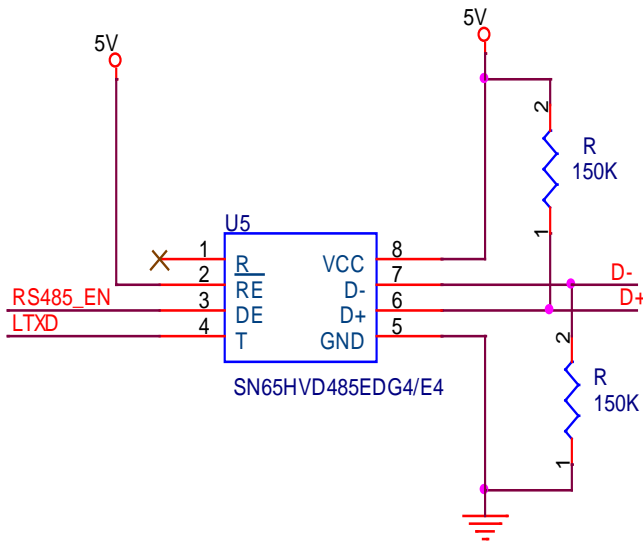
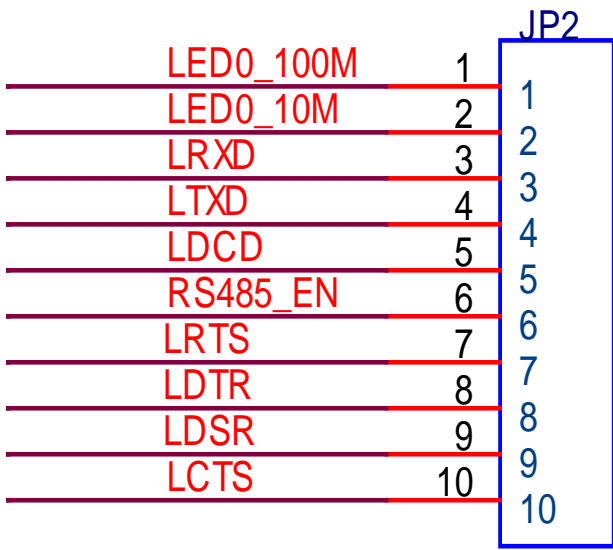


2W-RS-485 Circuit Design

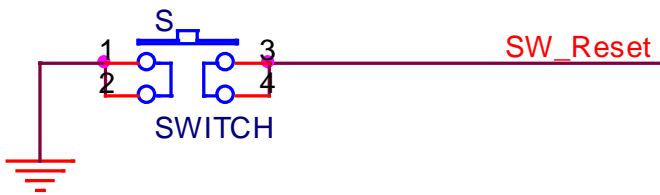
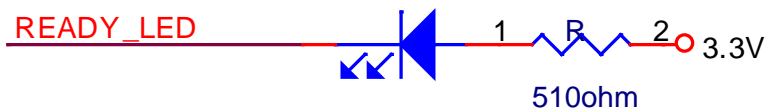
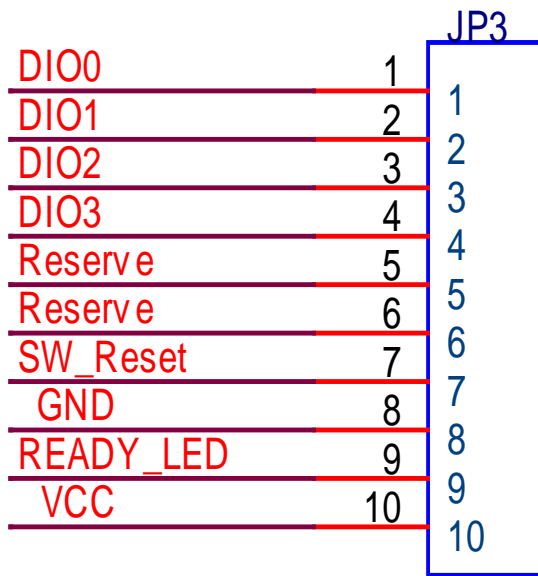




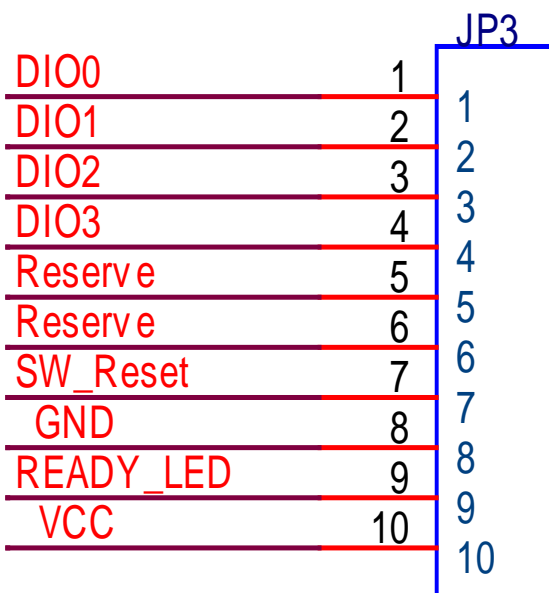
4W-RS-485 Circuit Design

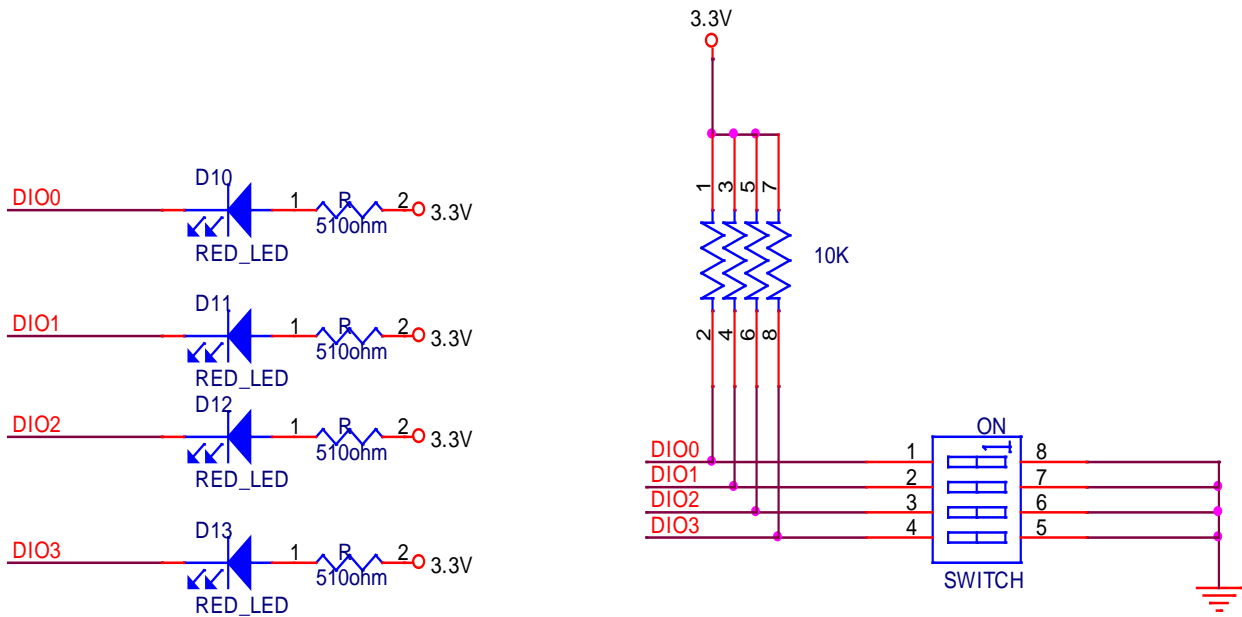


SW Reset and READY LED Circuit Design

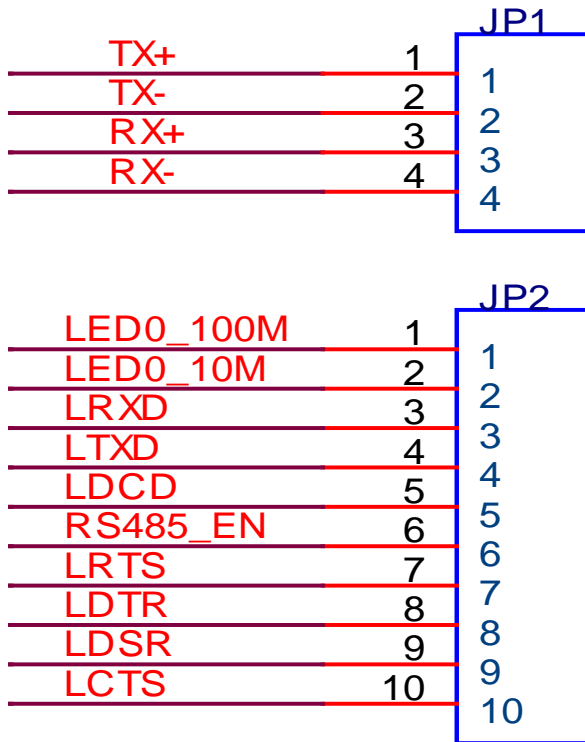


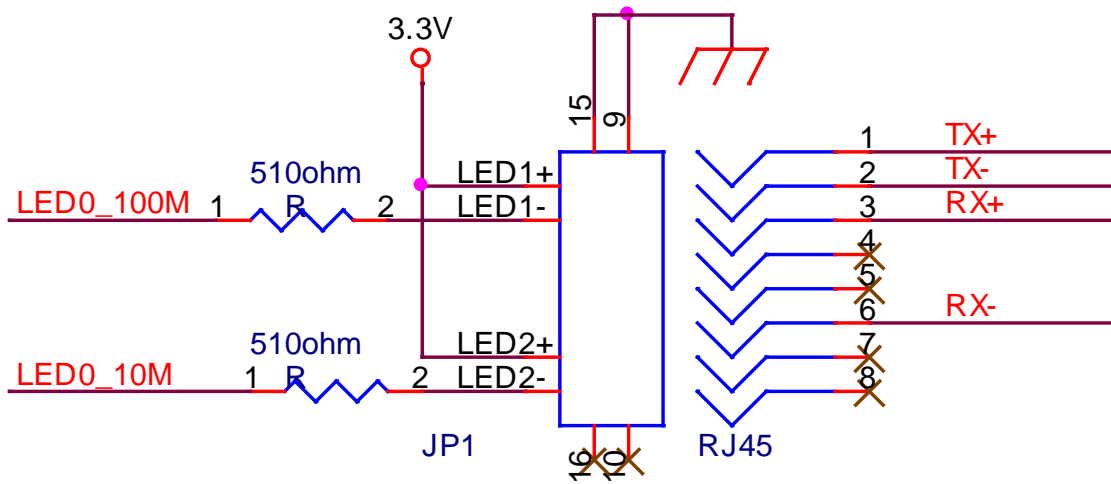
DIO Circuit Design





Ethernet Circuit Design





DC Characteristics for Serial PIO INTERFACE

Symbol	Parameter	Min	Nominal	Max	Units
VIL	Input Low Voltage	-0.3	N/A	0.8	V
VIH	Input High Voltage	2	N/A	5.5	V
VOL	Output Low Voltage	N/A	N/A	0.4	V
VOH	Output High Voltage	2.4	N/A	N/A	V
IOL	DIO/Other interface	11/5.6	N/A	N/A	mA
IOH	DIO/Other interface	12/7.2	N/A	N/A	mA
VIH	Input High Voltage	2	N/A	5.5	V